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PTO/SB/21 (08-00)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/751,526
	Filing Date	December 29, 2000
	First Named Inventor	Nandu J. Marketkar, et al.
	Group Art Unit	2631
	Examiner Name	Not yet assigned
Total Number of Pages in This Submission	Attorney Docket Number	42390P10081

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Firm or Individual name	Matthew C. Fagan, Reg. No. 37,542
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Date	5.30.2001

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Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	09/751,526
Filing Date	12-29-00
First Named Inventor	Marketkar, et al.
Group Art Unit	2631
Examiner Name	Not yet assigned
Attorney Docket Number	42390P10081

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/751,526
				Filing Date	12-29-00
				First Named Inventor	Marketkar, et al.
				Group Art Unit	2631
				Examiner Name	Not yet assigned
Sheet	2	of	2	Attorney Docket Number	42390P10081

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials[*]	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		Farjad-Rad, Ramin, et al., "A 0.3- μ m CMOS 8-Gb/s 4-PAM Serial Link Transceiver", IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, pp. 757-764 (May 2000).	
		Osaka, Hideki, High Performance Memory Interface for DDR-SDRAM II: XTL (Crosstalk Transfer Logic), Hitachi Ltd., Systems Development Laboratory, pp. 1-20 (September 15, 2000).	
		Stearns, Thomas H., Flexible Printed Circuitry, McGraw-Hill Companies, Inc., pp. 109-110 and 232-235 (1996).	
		XTL Evaluation System, Hitachi Ltd., Systems Development Laboratory, pp. 1-14 (September 15, 2000).	
		Yang, Chih-Kong Ken, et al., "A 0.5- μ m CMOS 4.0-Gbit/s Serial Link Transceiver with Data Recovery Using Oversampling", IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, pp. 713-722 (May 1998).	
		U.S. Patent Application No. 09/713,702, filed November 15, 2000, entitled INTERCONNECT MECHANICS FOR ELECTROMAGNETIC COUPLER, by Nandu J. Marketkar, Thomas F. Knight, Jr., John R. Benham, Mark E. Naylor and John L. Critchlow.	
		U.S. Patent Application No. 09/751,442, filed December 29, 2000, entitled CLAMP TO SECURE CARRIER TO DEVICE FOR ELECTROMAGNETIC COUPLER, by Nandu J. Marketkar, Mark E. Naylor and John L. Critchlow.	
		U.S. Patent Application No. 09/751,527, filed December 29, 2000, entitled ELECTROMAGNETIC COUPLER SOCKET, by Nandu J. Marketkar, Thomas F. Knight, Jr., Mark E. Naylor and John L. Critchlow.	

Examiner Signature		Date Considered	
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